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U.S. PATENT & TRADEMARK OFFICE
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(16356.737)

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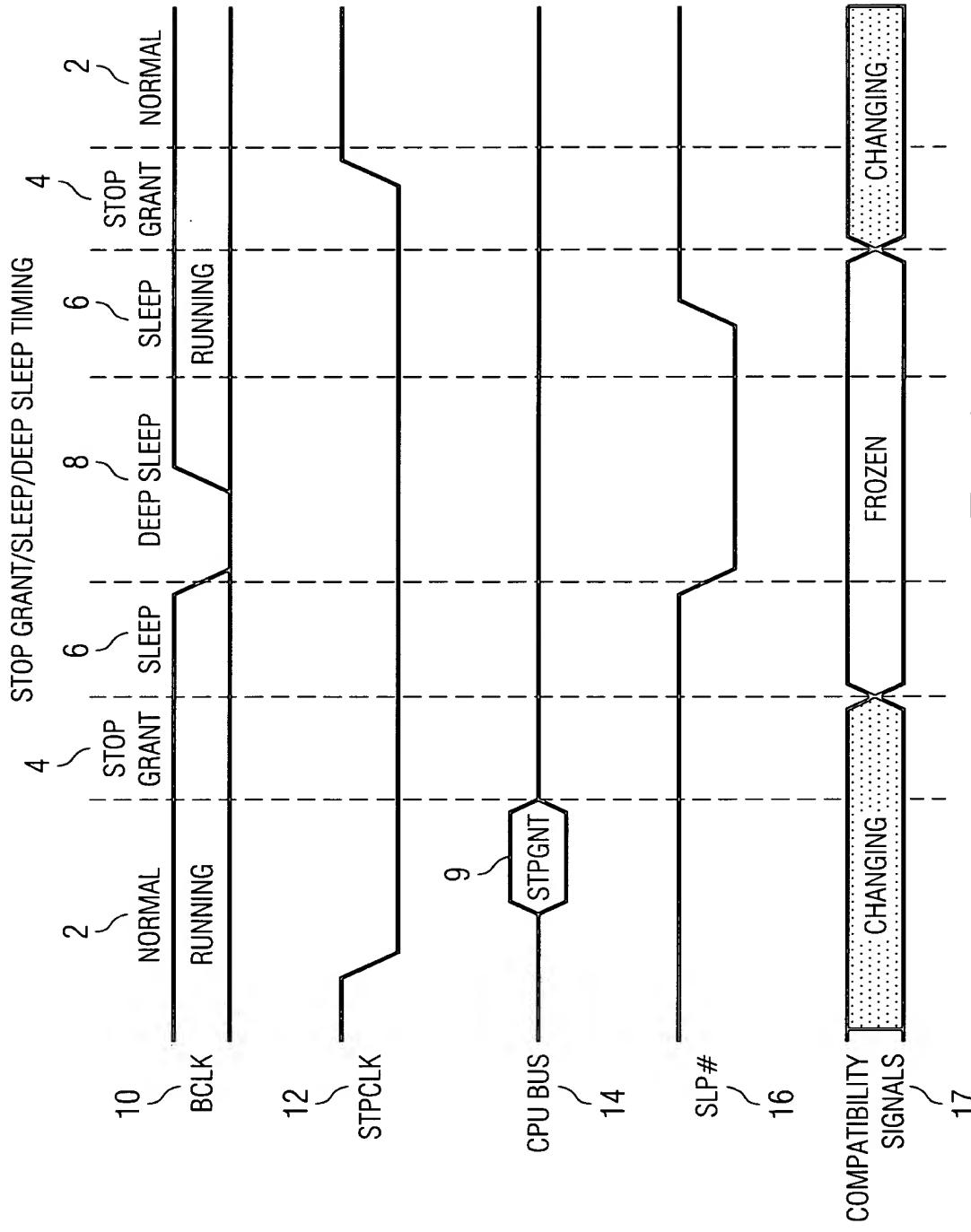


Fig. 1
(PRIOR ART)

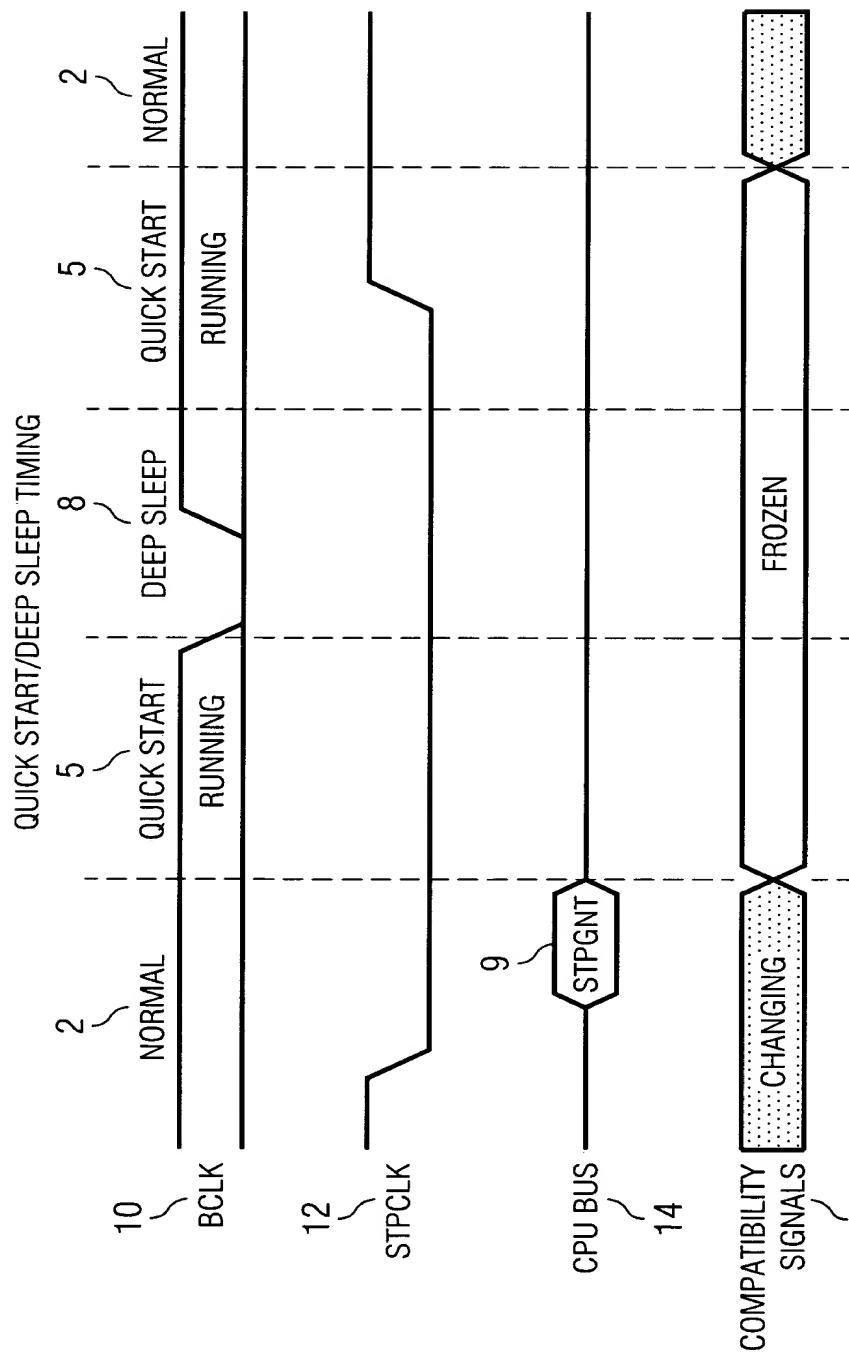


Fig. 2
(PRIOR ART)

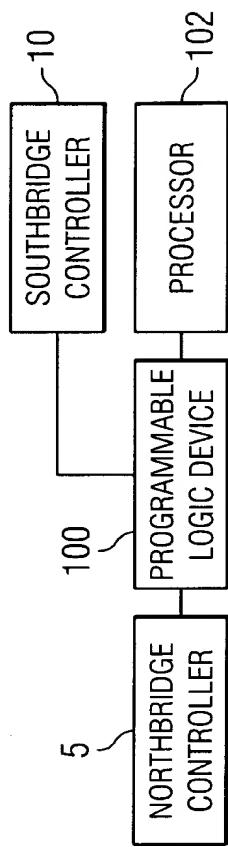


Fig. 3

TO Fig. 4B

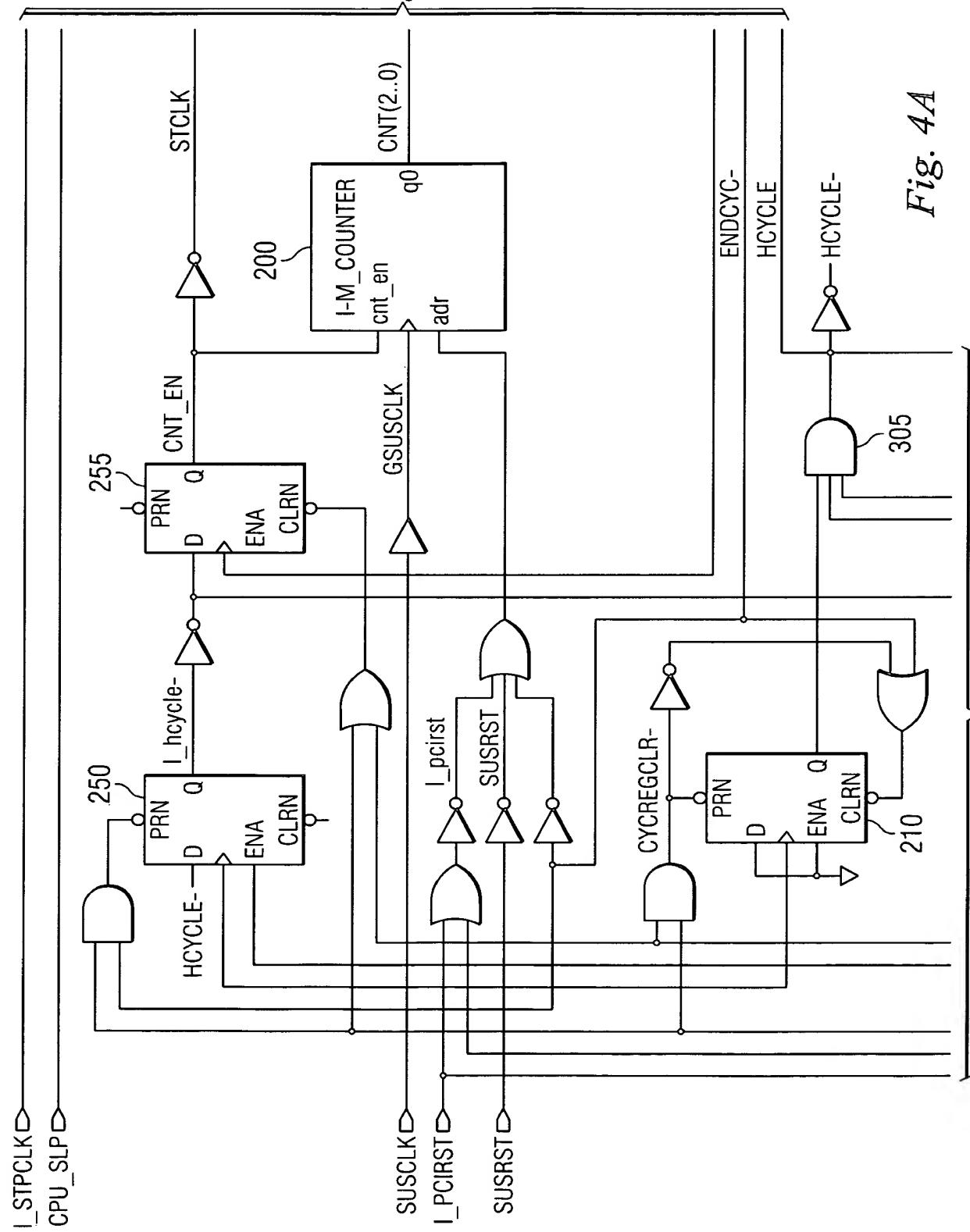
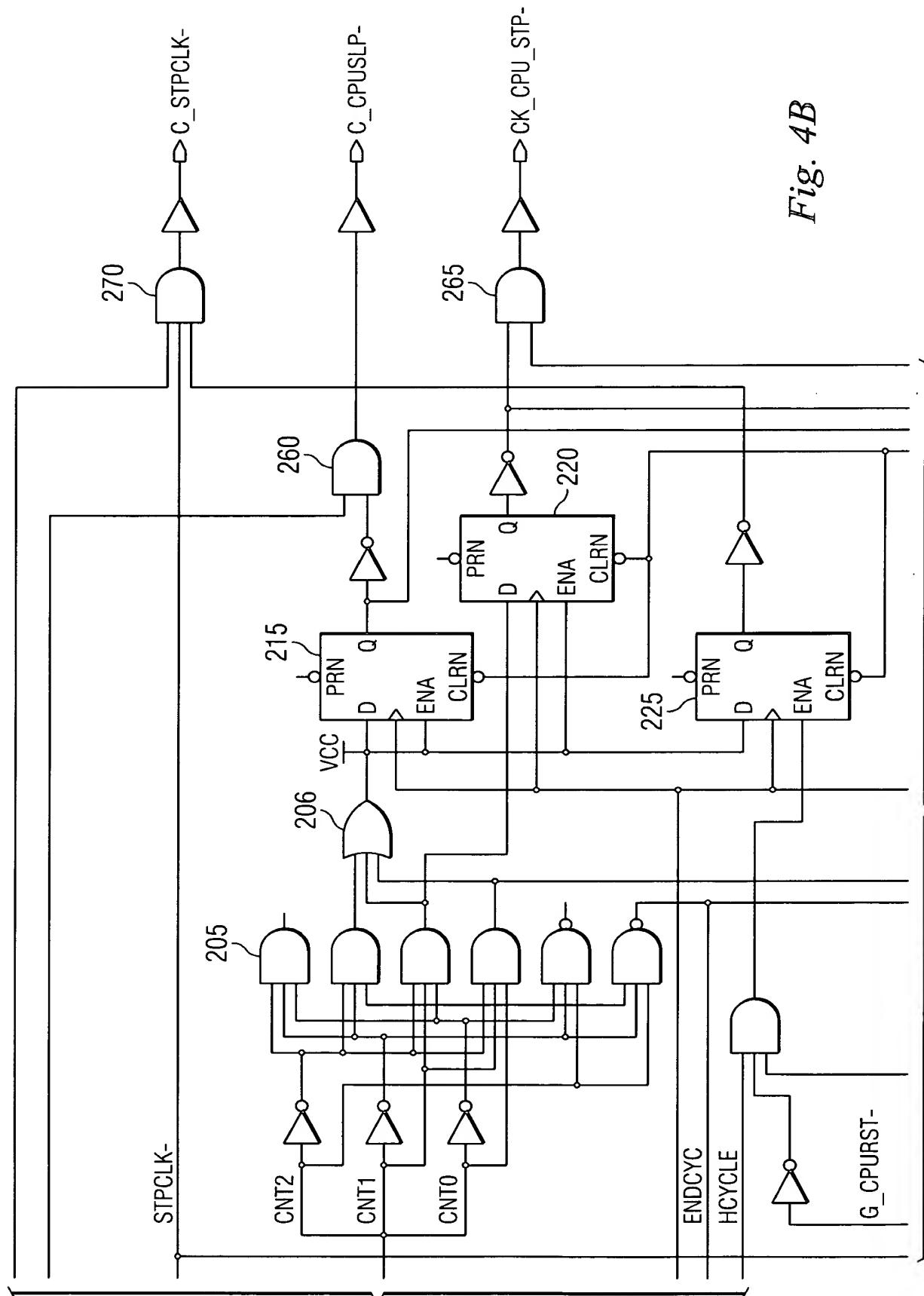


Fig. 4A

TO Fig. 4C



FROM Fig. 4A

Fig. 4B

TO Fig. 4D

TO Fig. 4D

Fig. 4C

FROM Fig. 4A

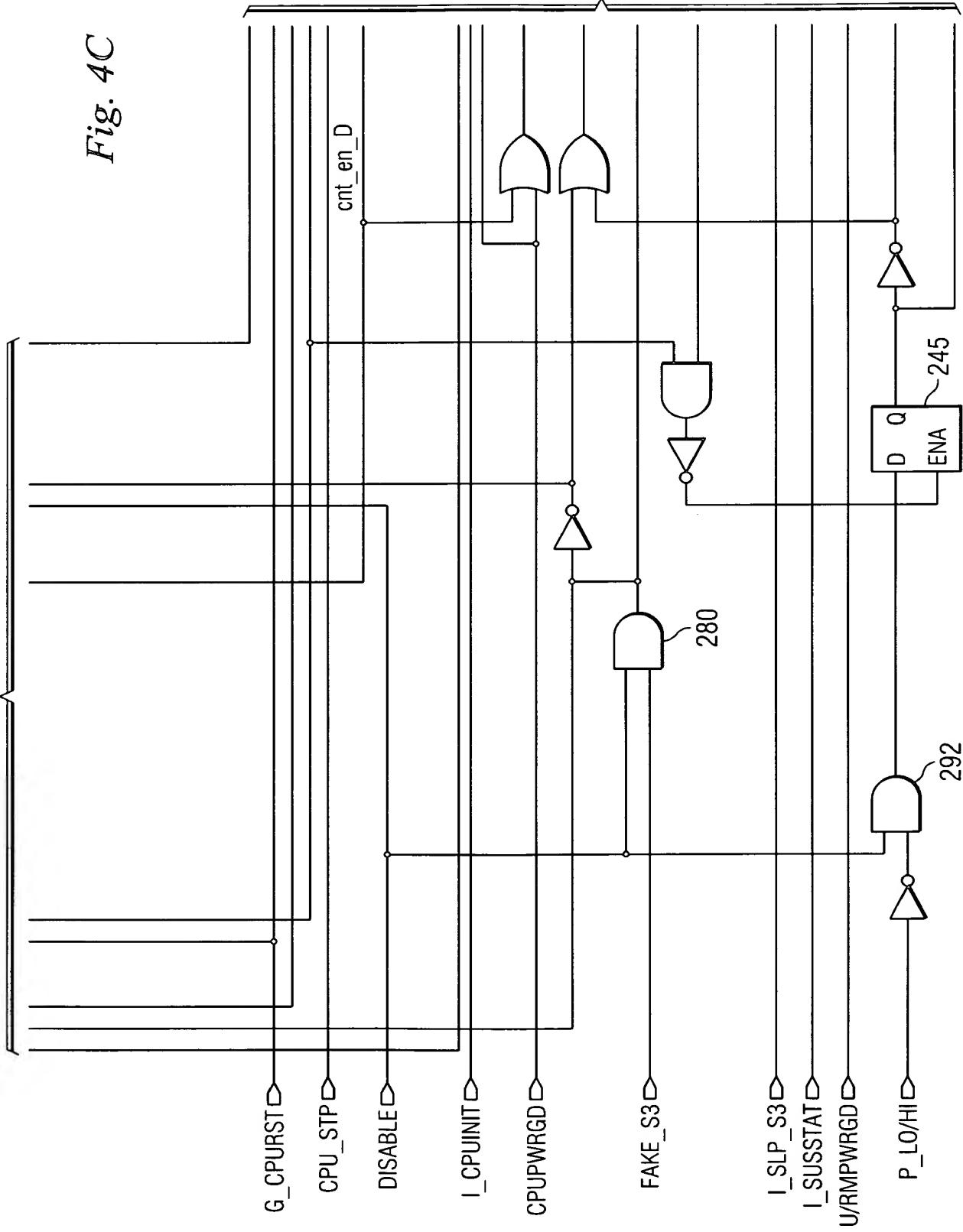
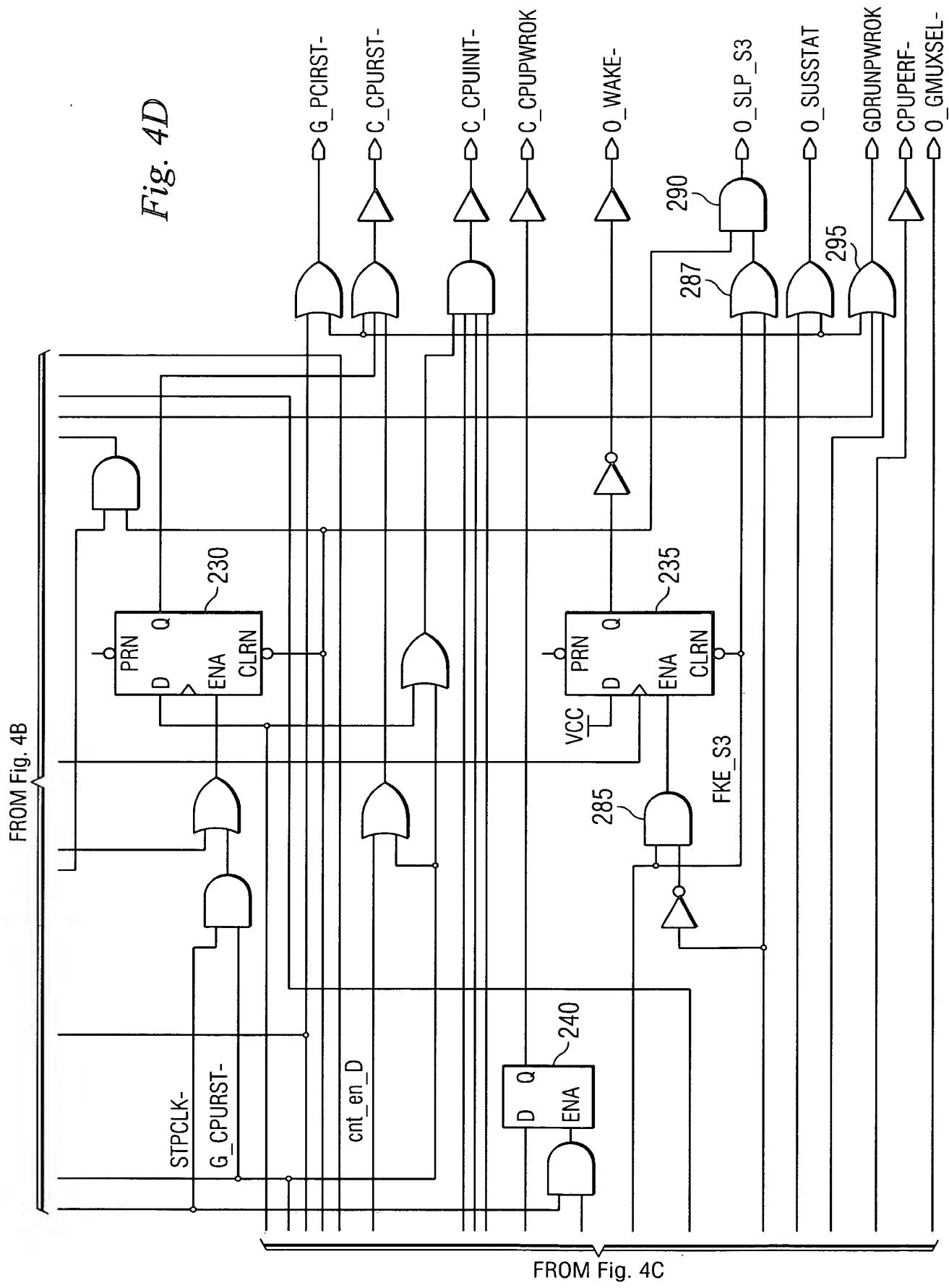
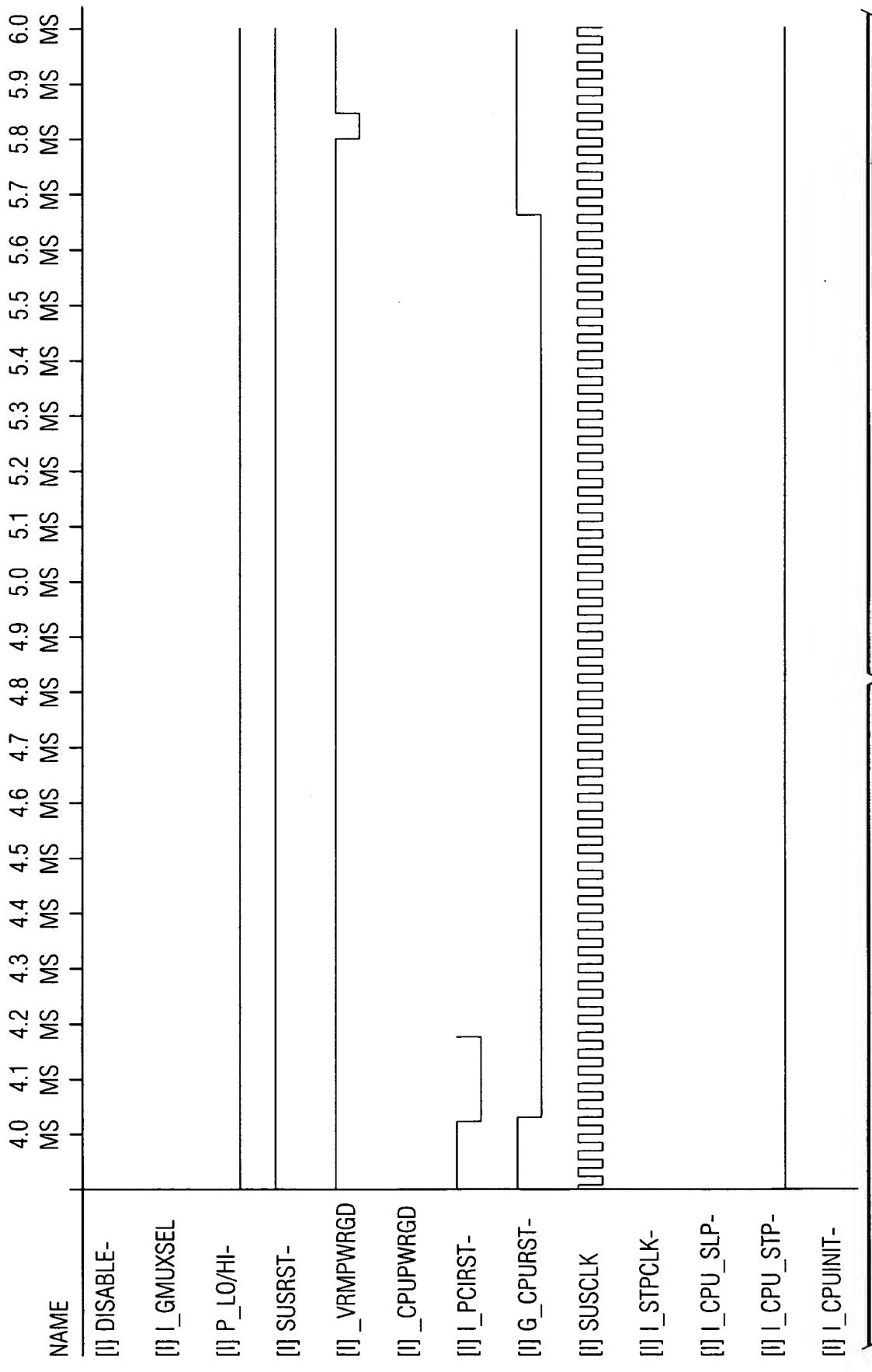


Fig. 4D



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Fig. 5A



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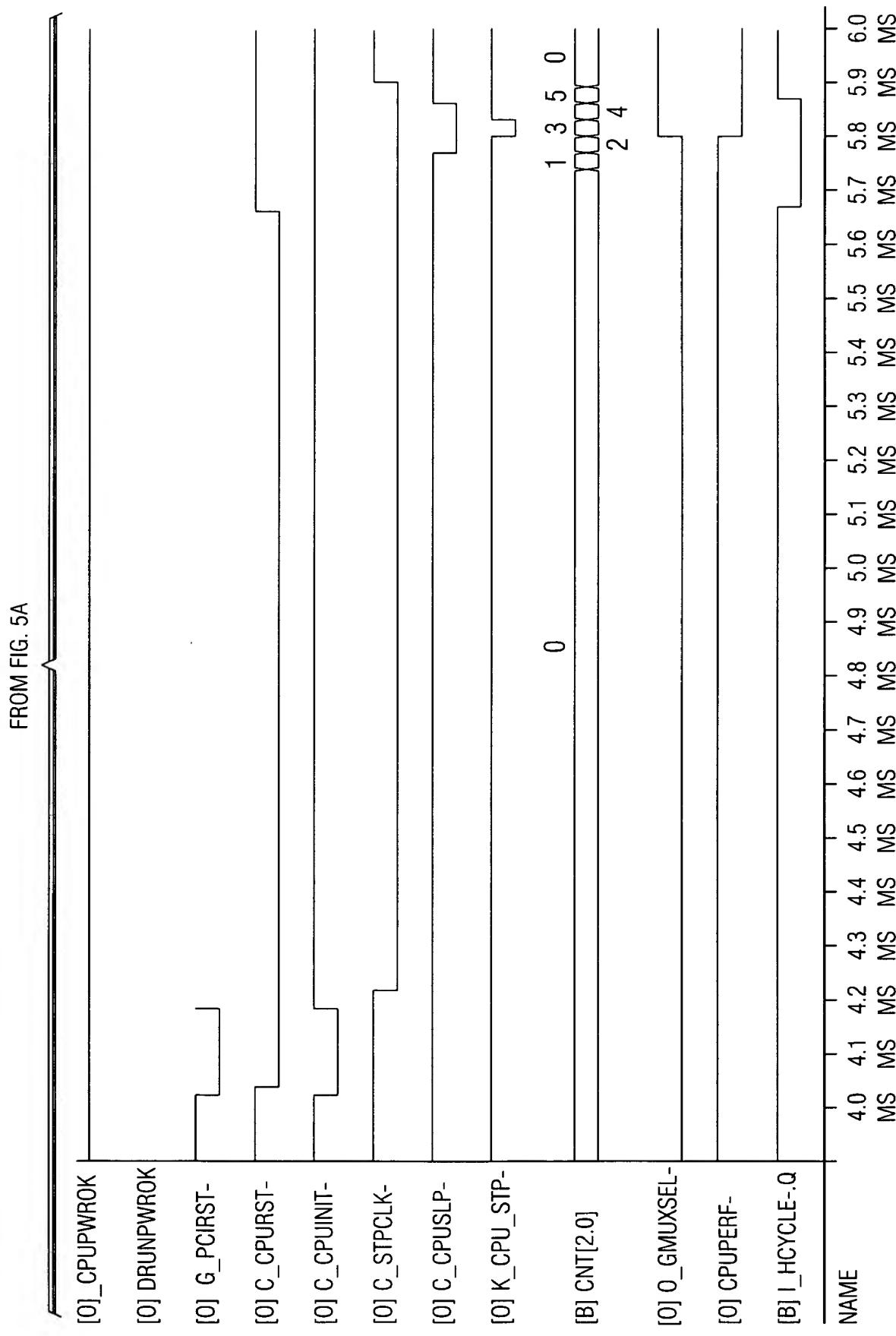
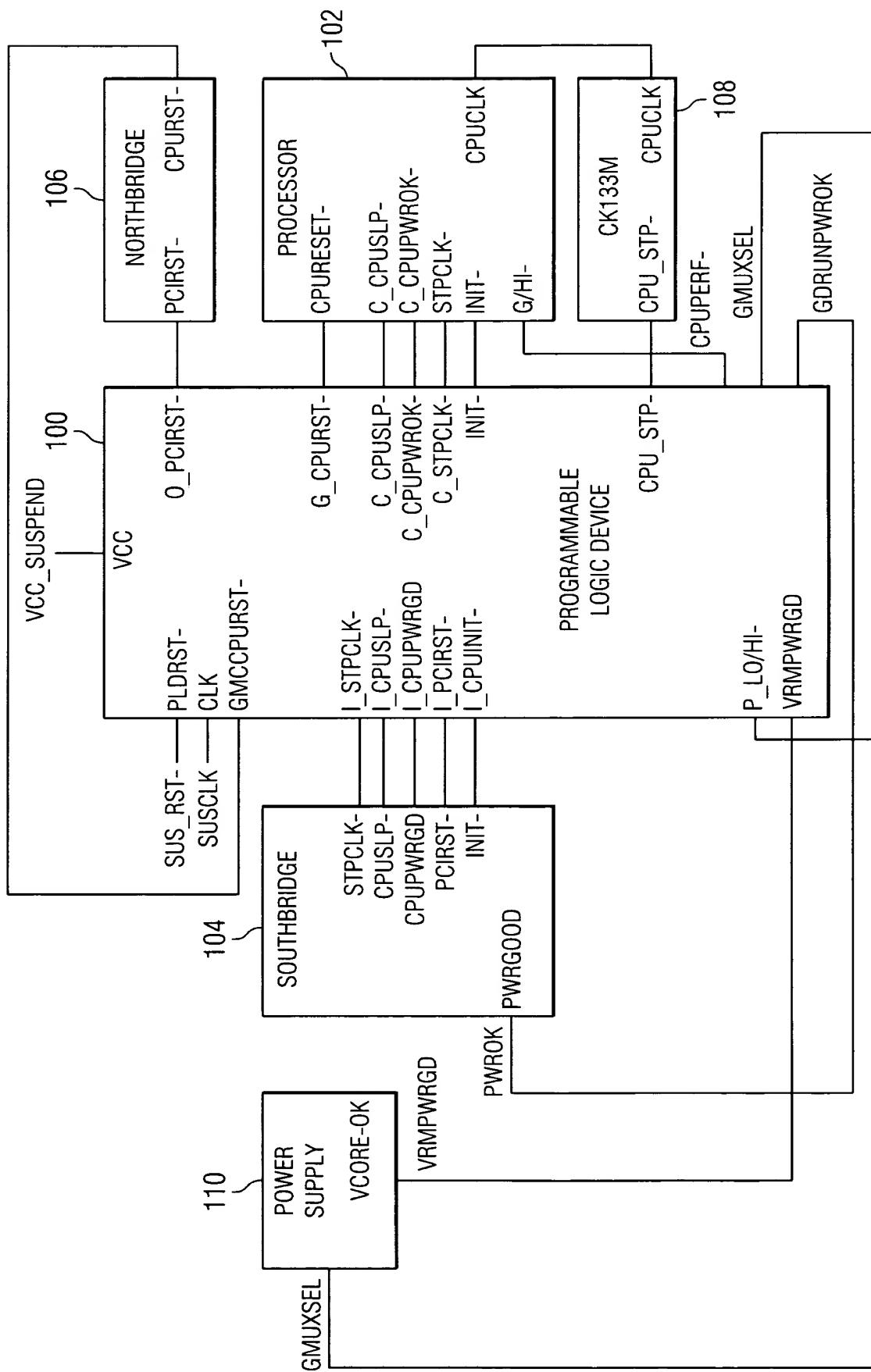


Fig. 5B



POWER MANAGEMENT CONTROLLER OR
NON-VOLATILE OUTPUT FROM CORE LOGIC

Fig. 6

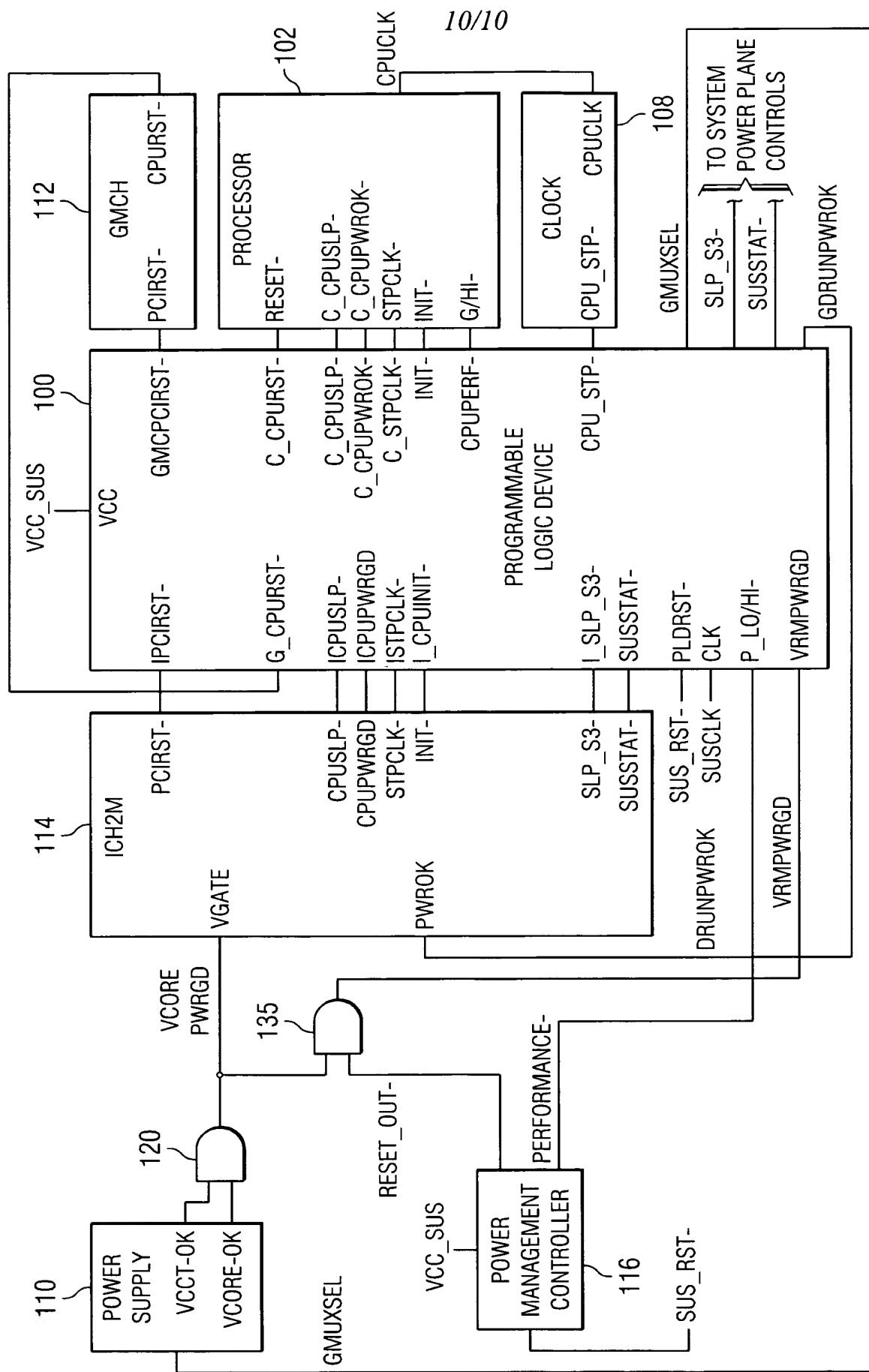


Fig. 7